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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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10/695,410

10/27/2003

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EXAMINER

TRAN, PHUC H

ART UNIT

PAPER NUMBER

2616

MAIL DATE

DELIVERY MODE

03/20/2008

PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary	Application No. 10/695,410	Applicant(s) JOHANSSON, SIXTEN	
	Examiner PHUC H. TRAN	Art Unit 2616	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 04 January 2008.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-18 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-18 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____ |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

1. The request for a continued prosecution filed on 01-04-2008 is acknowledged.

Claim Objections

2. Claims 1, 13, 15 and 18 are objected to because of the following informalities: “a single data computing device” should be rewritten as “a data computing device”, since claims directed to communications between units with a system as specification page 2, lines 1-7 that discloses a system comprises working and protecting units. Moreover the specification page 4, lines 18-20 does not support for the new language in claims.

Appropriate correction is required.

Claim Rejections - 35 USC § 102

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.

The changes made to 35 U.S.C. 102(e) by the American Inventors Protection Act of 1999 (AIPA) and the Intellectual Property and High Technology Technical Amendments Act of 2002 do not apply when the reference is a U.S. patent resulting directly or indirectly from an international application filed before November 29, 2000. Therefore, the prior art date of the reference is determined under 35 U.S.C. 102(e) prior to the amendment by the AIPA (pre-AIPA 35 U.S.C. 102(e)).

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4. Claims 1-18 are rejected under 35 U.S.C. 102(e) as being anticipated by Shabtay et al. (U.S. Patent No. 7093027 B1).

- With respect to claims 1, 13-15 and 18, Shabtay teaches a system for performing a switch-over in data communication (e.g. fast protection mechanism in Fig. 3) in accordance with a protection switching data communication principles (e.g. the system in Fig. 3 for protection data communication), said system comprising a single data computing device arranged to operate (e.g. Fig. 3 shows the local stack 51), in a data network according to the protection switching data communication principles, the single data computing device comprising: a configurable integrated circuit (e.g. edge switch 20 has block 30 in Fig. 1) of a unit of the data communication for signaling a need (e.g. the signal to determine the health of the path see col. 10, lines 56-63) for the switch-over in real time (col. 12, lines 21-22 shows the 50ms switching and col. 2, lines 54-57 show the real-time switching for voice and video) based data communication to a configurable integrated circuit of a protecting pair unit (e.g. Edge switch B in Fig. 1; and Fig. 3) of said unit of the data communication; wherein said configurable integrated circuit of said protecting pair unit is structured and arranged to perform the switch-over independently of a CPU, when the switch-over is need (e.g. the switch-over is independently of CPU as col. 10, lines 5-15).

- With respect to claim 2, Shabtay teaches wherein the system provides the signaling between the units without a participation of CPU (e.g. the switch 30 implement as a network processor or FPGAs or ASIC to switch the data from user 22 to network port 24 in Fig. 1).

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- With respect to claim 3, Shabtay also teaches wherein the configurable integrated circuit comprises at least one of application-specific integrated circuit (ASIC) and field-programmable gate array (FPGA) (e.g. col. 10, line 11).

- With respect to claim 4, Shabtay further teaches wherein the protection switching comprises a protected LSP based on a working connection and a protecting connection (see col. 8, lines 47-56; col. 9, lines 19-21).

- With respect to claim 5, Shabtay discloses wherein said unit comprises a working unit in accordance with a LSP working connection and the protection pair unit comprises a protection unit in accordance with a LSP protection connection (e.g. the fault link between Edge switch A and B would consider as working unit and Edge C as protection unit in Fig. 3).

- With respect to claim 6, Shabtay teaches wherein the signal comprises a protection message for delivering that the data communication of a receiving unit is at least one of faulty and unfaulty (e.g. col. 10, lines 58-62, col. 11, lines 15-25).

- With respect to claim 7, Shabtay teaches wherein the real time based data communication presumes the switch-over to take place in less than 50 milliseconds from an occurrence of a connection fault (e.g. col. 12, lines 21-22; col. 4, line 51).

- With respect to claim 8, Shabtay also teaches wherein the data communication comprises at least one of Internet Protocol, Ethernet, and MPLS for real time telecommunication services (e.g. col. 4, line 53).

- With respect to claim 9, Shabtay further teaches wherein Multiprotocol Label Switching is contained as a bearer for the data communication (e.g. col. 1, lines 25-45).

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- With respect to claim 10, Shabtay teaches wherein Multiprotocol Label Switching operates as a backbone for IP based data communication (e.g. col. 1, lines 26-28).

- With respect to claim 11, Shabtay also teaches wherein the real time based data communication is such that human senses any application based on the real time based data communication substantially immediate (e.g. the fast local protection as sub 50 ms teaches in col. 4, line 51).

- With respect to claim 12, Shabtay teaches wherein the data communication takes place between a source computing entity and a sink computing entity (e.g. the Edge switch in Fig. 3).

- With respect to claim 16, Shabtay teaches before the step of signaling the step of detecting a connection fault in the data communication at the unit (e.g. the Hello message to monitor the health of the path col. 10, lines 58-60).

- With respect to claim 17, Shabtay teaches the step of receiving the need at the protecting pair unit and performing the switch over by activating the data communication on the protecting pair unit (e.g. the Edge switch B switchover to Edge switch C as Fig. 3 shows).

Response to Amendment

5. Applicant's arguments filed 7/9/07 have been fully considered but they are not persuasive.

In response to Applicant argument that "Shabtay fails to suggest a device having a least two configurable integrated circuits (e.g. two ASICs) that communicate with

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each other. Examiner respectfully disagrees. The two configurable integrated circuits would be the Edge Switches A and B in Fig. 1.

In response to applicant's argument that the references fail to show certain features of applicant's invention, it is noted that the features upon which applicant relies (i.e., two ASICs) are not recited in the rejected claim(s). Although the claims are interpreted in light of the specification, limitations from the specification are not read into the claims. See *In re Van Geuns*, 988 F.2d 1181, 26 USPQ2d 1057 (Fed. Cir. 1993).

Conclusion

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to PHUC H. TRAN whose telephone number is (571) 272-3172. The examiner can normally be reached on M-F (8-4:30).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, CHI PHAM can be reached on (571) 272-3179. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/PHUC H TRAN/

Examiner, Art Unit 2616